

Notice of References Cited	Application/Control No. 10/025,322	Applicant(s)/Patent Under Reexamination KUSUMOTO ET AL.	
	Examiner Julio J. Maldonado	Art Unit 2823	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,162,667	12-2000	Funai et al.	438/166
*	B	US-4,795,679	01-1989	Ramesh et al.	428/428
*	C	US-5,648,282	07-1997	Yoneda, Kenji	438/305
*	D	US-5,365,875	11-1994	Asai et al.	438/487
*	E	US-4,552,595	11-1985	Hoga, Hiroshi	438/440
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ghandhi et al., VLSI Fabrication Principles, 1983 John Wiley and Sons, Inc., pages 517-520
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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